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Mark L. Gleason Dicke, Billig & Czaja, P.A. 701 Building, Suite 2250 701 Fourth Avenue South Minneapolis, MN 55415

In re Application of:

Kshirsagar, etal

Application No.: 10/568,842

PCT No.: PCT/DE2004/001799

Int. Filing Date: 11 August 2004

Priority Date: 20 August 2003

Atty. Docket No.:

I431.151.101/FIN504PCT/US

For: Electrical Diagnostic Circuit and

Method for the Testing and/or

Diagnostic Analysis of an Integrated

Circuit

Dear Mr. Gleason:

This letter is in response to your communication received 16 November 2007, requesting the status of the Filing Receipt for U.S. national application 10/568,842.

The United States Designated Office/Elected Office (DO/EO) has docketed the U.S. national application for preparation of the Filing Receipt. However, the USPTO is currently experiencing a backlog of applications awaiting examination. The filing receipt should issue in due course. The appropriate personnel of the Designated Office/Elected Office (DO/EO) to which this application has been assigned have been notified of your request.

Stacy Duncan

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Supervisory Legal Examiner

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STATUS REQUEST